



## PRODUCT OVERVIEW

The AT series offer up to 30 watts of output power in standard 2.00 x 1.00 x 0.4 inches packages. This series features high efficiency and 1500 Volts of DC isolation. The AT series provides a 4:1 wide input voltage range of 9 to 36 or 18 to 75VDC, and delivers accurate regulated output. These modules operate over the ambient operating temperature range of -40°C to +85°C. These converters are fully protected against input UVLO (Under Voltage Lock Out), over-current, over-voltage and continuous short circuit protection. In addition, the option control functions include Positive Remote On/Off and adjustable output voltage.

## FEATURES

- Industry standard footprint (2 inch X 1 inch)
- Regulated Outputs, Fixed Switching Frequency
- Up to 92 % Efficiency
- 4:1 Input Range
- Up to 30 Watts of Power
- -40°C to +85°C temperature range
- Remote On/Off logic control (Option)
- No Tantalum Capacitors
- Continuous Short Circuit and Over Current Protection

## APPLICATIONS:

- Distributed Power Architecture
- Mobile telecommunication
- Industrial applications
- Battery operated equipment

## AVAILABLE OPTIONS

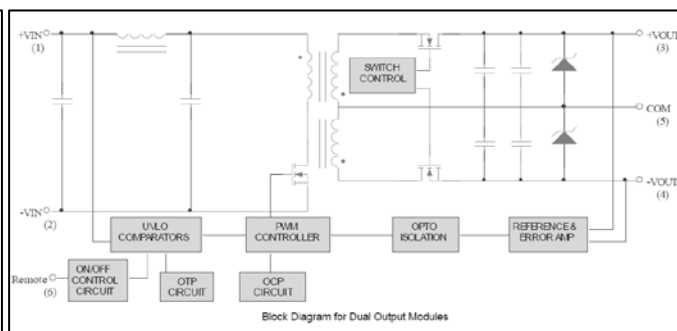
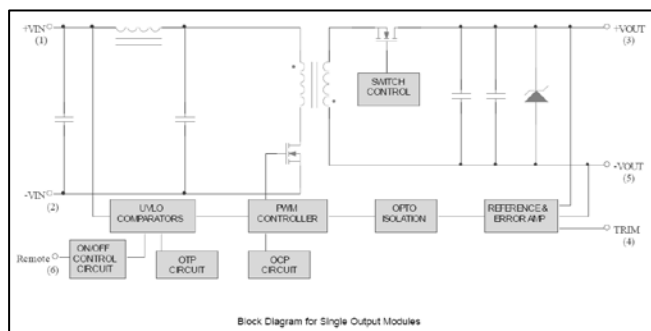
- Customizable output voltages
- CE Mark 2004/108/EC certification
- UL60950-1, EN60950-1, and IEC60950-1 safety

Contact DATEL for other series in 2.00" x 1.00" footprint

- Cost Savings, Lower Power, Other Voltage outputs, Higher Efficiency, etc.

MODEL NUMBER	INPUT VOLTAGE	OUTPUT VOLTAGE	OUTPUT CURRENT MAX	EFFICIENCY %	LOAD REGULATION	LINE REGULATION
AT22S3.3-7.5	9-36 VDC	3.3VDC	7.5 A	88	± 0.5 %	± 0.2 %
AT22S5-6	9-36 VDC	5.0 VDC	6 A	89	± 0.5 %	± 0.2 %
AT22S12-2.5	9-36 VDC	12 VDC	2.5 A	91	± 0.5 %	± 0.2 %
AT22S15-2	9-36 VDC	15 VDC	2 A	91	± 0.5 %	± 0.2 %
AT22D12-1.25	9-36 VDC	±12 VDC	±1.25 A	87	±1 %	± 0.5 %
AT22D15-1	9-36 VDC	±15 VDC	±1 A	88	±1 %	± 0.5 %
AT45S3.3-7.5	18-75VDC	3.3 VDC	7.5 A	87	± 0.5 %	± 0.2 %
AT45S5-6	18-75VDC	5 VDC	6 A	89	± 0.5 %	± 0.2 %
AT45S12-2.5	18-75VDC	12 VDC	2.5 A	91	± 0.5 %	± 0.2 %
AT45S15-2	18-75VDC	15 VDC	2 A	92	± 0.5 %	± 0.2 %
AT45D12-1.25	18-75VDC	±12 VDC	±1.25 A	91	±1 %	± 0.5 %
AT45D15-1	18-75VDC	±15 VDC	±1 A	92	±1 %	± 0.5 %

## FUNCTIONAL BLOCK DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

Parameters	Conditions	Model	Min.	Typical	Max.	Units
<b>Input Voltage</b>						
Continuous	DC	24V <sub>in</sub>	0		36	Volts
		48V <sub>in</sub>	0		75	
Transient	100ms, DC	24V <sub>in</sub>			50	Volts
		48V <sub>in</sub>			100	
Operating Ambient Temperature	Derating, Above 78°C	All	-40		+85	°C
Case Temperature		All			+105	°C
Storage Temperature		All	-55		+125	°C
Input / Output Isolation Voltage	1 minute	All			1500	Volts

## INPUT CHARACTERISTICS

Note: All specifications are typical at nominal input, full load at 25°C unless otherwise noted

Parameters	Conditions	Model	Min.	Typical	Max.	Units
Operating Input Voltage		24V <sub>in</sub> 48V <sub>in</sub>	9 18	24 48	36 75	Volts
Maximum Input Current	100% Load, V <sub>in</sub> =9V	24V <sub>in</sub>		3800		mA
	100% Load, V <sub>in</sub> =18V	48V <sub>in</sub>		1900		
No-Load Input Current	V <sub>in</sub> =Nominal input	AT22S3.3-7.5 AT22S5-6 AT22S12-2.5 AT22S15-2 AT22D12-1.25 AT22D15-1 AT45S3.3-7.5 AT45S5-6 AT45S12-2.5 AT45S15-2 AT45D12-1.25 AT45D15-1		100 110 50 50 60 60 50 50 30 30 40 40		mA
Input UnderVoltage Lockout						
Turn-On Voltage Threshold		24V <sub>in</sub>	8	8.5	8.8	VDC
		48 V <sub>in</sub>	16.5	17	17.5	VDC
Turn-Off Voltage Threshold		24V <sub>in</sub>	7.7	8.5	8.8	VDC
		48 V <sub>in</sub>	15.5	16	16.5	VDC
Lockout Hysteresis Voltage		24V <sub>in</sub>		0.5		VDC
		48 V <sub>in</sub>		0.9		
Inrush Current (I <sup>2</sup> t)	AT per ETS300 132-2	All			0.1	A²s
Input Reflected-Ripple Current	P-P thru 12uH inductor, 5Hz to 20MHz	All			30	mA

### OUTPUT CHARACTERISTIC

Parameters	Conditions	Model	Min.	Typical	Max.	Units
Output Voltage Set Point	$V_{in}$ =Nominal $V_{in}$ , $I_o = I_{o\_max}$ , $T_c=25^{\circ}C$	$V_o=3.3$	3.267	3.3	3.333	Volts
		$V_o=5.0$	4.95	5	5.05	
		$V_o=12$	11.88	12	12.12	
		$V_o=15$	14.85	15	15.15	
		$V_o=\pm 12$	$\pm 11.88$	$\pm 12$	$\pm 12.12$	
		$V_o=\pm 15$	$\pm 14.85$	$\pm 15$	$\pm 15.15$	
Output Voltage Balance	$V_{in}$ =nominal, $I_o = I_{o\_max}$ , $T_c=25^{\circ}C$	Dual			$\pm 1.0$	%
Output Voltage Regulation						
Line Regulation	$V_{in}$ =High line to Low line Full Load	Single			$\pm 0.2$	%
		Dual			$\pm 0.5$	%
Load Regulation	$I_o$ = Full Load to min. Load	Single			$\pm 0.5$	%
		Dual			$\pm 1.0$	%
Temperature Coefficient	$T_c=-40^{\circ}C$ to $80^{\circ}C$				$\pm 0.02$	%/ $^{\circ}C$
Cross Regulation	Load cross variation 10%/100%	Dual			$\pm 5$	%
Output Voltage Ripple and Noise						
Peak-to-Peak	Full Load, 20MHz bandwidth 0.1uF ceramic capacitor	$V_o=3.3V$			75	mV
		$V_o=5V$				
		$V_o=12V$			100	
		$V_o=15V$				
		$V_o=\pm 15V$				
		$V_o=\pm 12V$				
Operating Output Current Range		$V_o=3.3V$	0		7500	mA
		$V_o=5V$	0		6000	
		$V_o=12V$	0		2500	
		$V_o=15$	0		2000	
		$V_o=\pm 12V$	0		$\pm 1250$	
		$V_o=\pm 15V$	0		$\pm 1000$	
Output DC Current-Limit Inception	Output Voltage=90% $V_{o\_nominal}$	All	110	140	160	%
Maximum Output Capacitance	Full load, Resistance	$V_o=3.3V$			7500	$\mu F$
		$V_o=5V$			6000	
		$V_o=12V$			2500	
		$V_o=15V$			2000	
		$V_o=\pm 12V$			1250	
		$V_o=\pm 15V$			1000	

### DYNAMIC CHARACTERISTICS

Parameters	Conditions	Model	Min.	Typical	Max.	Units
Output Voltage Current Transient						
Step Change in Output Current	75% to 100% of $I_{o\_max}$	All			$\pm 5$	%
Setting Time (within 1% $V_{o\_nominal}$ )	$di/dt=0.1A/us$	All			250	$\mu s$
Turn-On Delay and Rise Time						
Turn-On Delay Time, From Input	$V_{in\_min}$ to 10% $V_{o\_set}$	$V_{in}=24V$ $V_{in}=48V$		3 3		ms
Turn-On Delay Time, From On/Off Control	$V_{on/off}$ to 10% $V_{o\_set}$	All		2.5		ms
Output Voltage Rise Time	10% $V_{o\_set}$ to 90% $V_{o\_set}$	$V_{in}=24V$ $V_{in}=48V$		3 3		ms

## FEATURE CHARACTERISTICS

Parameters	Conditions	Model	Min.	Typical	Max.	Units
Efficiency 100% Load	$V_{in} = 24 \text{ Vdc}$ , $I_o = I_{o\_max}$ , $T_c = 25^\circ\text{C}$	AT22S3.3-7.5 AT22S5-6 AT22S12-2.5 AT22S15-2 AT22D12-1.25 AT22D15-1		87 90 91 92 91 92		%
	$V_{in} = 48 \text{ Vdc}$ , $I_o = I_{o\_max}$ , $T_c = 25^\circ\text{C}$	AT45S3.3-7.5 AT45S5-6 AT45S12-2.5 AT45S15-2 AT45D12-1.25 AT45D15-1		88 90 90 91 90 91		%
ISOLATION CHARACTERISTICS						
Input to Output	1 minutes	All			1500	Volts
Isolation Resistance		All	1000			MΩ
Isolation Capacitance		All		1000		pF
Switching Frequency		$V_{in} = 24\text{V}$		430		KHz
		$V_{in} = 48$		430		
On/Off Control (Option P) , Positive Remote On/Off logic						
Logic High (Module On)	$V_{on/off}$ at $I_{on/off} = 0.1\mu\text{A}$	All	3.5 or Open Circuit		75	Volts
Logic Low (Module Off)	$V_{on/off}$ at $I_{on/off} = 0.1\mu\text{A}$	All			1.2	Volts
Output Voltage Trim range (Option T)	At rated Power	All	-10		+10	%
ON/OFF Current	$I_{on/off}$ at $V_{on/off} = 0.0\text{V}$			0.3	1	mA
Leakage Current	Logic High, $V_{on/off} = 15\text{V}$				30	μA
Output Over Voltage Protection	Zener or TVS Clamp	$V_o = 3.3\text{V}$		3.9		VDC
		$V_o = 5\text{V}$		6.2		
		$V_o = 12\text{V}$		15		
		$V_o = 15\text{V}$		18		
		$V_o = \pm 12\text{V}$		±15		
		$V_o = \pm 15\text{V}$		±18		
MTBF	$I_o = 100\%$ of $I_{o\_max}$ ; $T_a = 25^\circ\text{C}$ per MIL-HDBK-217F	Single Dual		900 650		K hours
Weight		All		35		grams

### Operating Temperature Range

The AT series of converters operates over the wide temperature of -40°C to +85°C. This module starts to derate above +65°C. The module operate normally up to +105°C case temperature.

### Output Voltage Adjustment

The output voltage on the T option models is adjustable within the range of -10% to +10%.

### Remote ON/OFF (Option)

The remote ON/OFF input feature of the converter allows external circuitry to turn the converter ON or OFF. Active-high remote ON/OFF is available as standard. The converter is turned on if the remote ON/OFF pin is high (>3.5Vdc to 75Vdc or open circuit). Setting the pin low (<1.2Vdc) will turn the converter 'Off'. The signal level of the remote on/off input is defined with respect to "-Vin". If not using the remote on/off pin, leave the pin open (module will be on).

### UVLO (Under Voltage Lock Out)

Input under voltage lockout is standard on the AT unit. The unit will shut down when the input voltage drops below a threshold, and the unit will operate when the input voltage goes above the upper threshold.

### Over Current Protection

All models have internal over current and continuous short circuit protection. The unit operates normally once the fault condition is removed. At the point of current limit inception, the converter will go into hiccup mode protection.

### Over Voltage Protection

The over-voltage protection consists of a Zener diode to limit the output voltage.

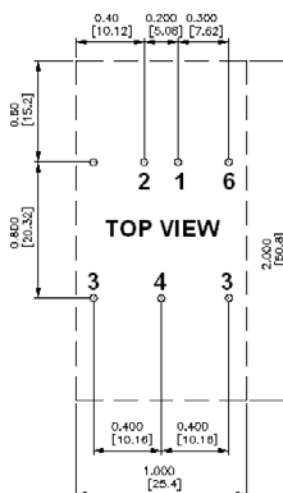
### Over-Temperature Protection (OTP)

The AT series converters are equipped with non-latching over-temperature protection. If the temperature exceeds a threshold of 110°C (typical) the converter will shut down, disabling the output. When the temperature has decreased the converter will automatically restart. The over-temperature condition can be induced by a variety of reasons such as external overload condition or a system fan failure.

### Recommended Layout PCB Footprints and Soldering Information

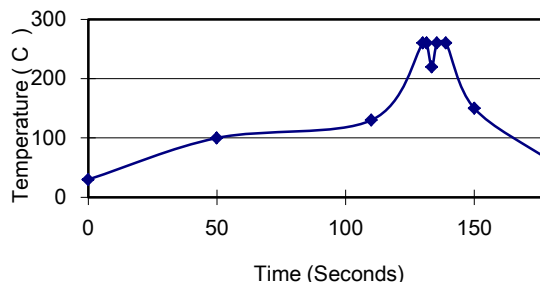
The end user of the converter must ensure that other components and metal in the vicinity of the converter meet the spacing requirements to which the system is approved. Low resistance and low inductance PCB layout traces should be used where possible. Careful consideration must also be given to proper low impedance tracks between power module, input and output grounds. The recommended footprints and soldering profiles are shown in the next two figures

Standard PIN Configuration  
1.3mm PLATED THROUGH HOLE  
2.0mm PAD SIZE



**Recommended PCB Layout Footprints, Dimensions are in inches (mm)**

**Lead Free Wave Soldering Profile**



**Wave Soldering Profiles**

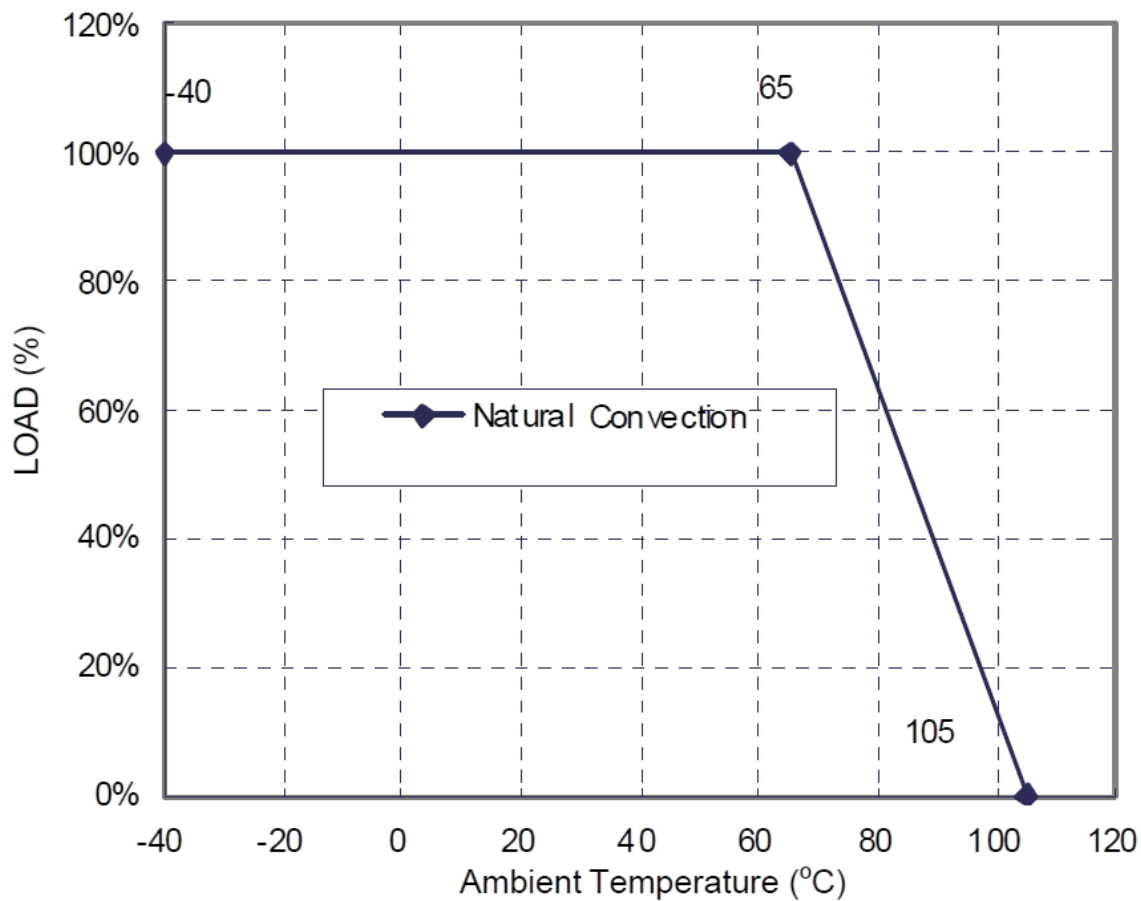
Note :

1. Soldering Materials: Sn/Cu/Ni
2. Ramp up rate during preheat: 1.4 °C/Sec (From 50°C to 100°C)
3. Soaking temperature: 0.5 °C/Sec (From 100°C to 130°C), 60±20 seconds
4. Peak temperature: 260°C, above 250°C 3~6 Seconds
5. Ramp up rate during cooling: -10.0 °C/Sec (From 260°C to 150°C)

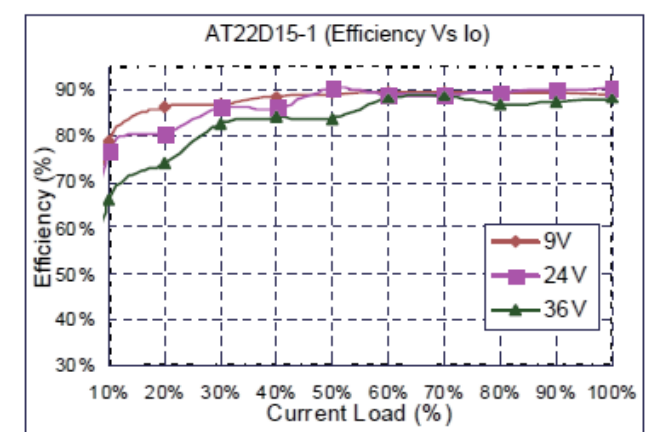
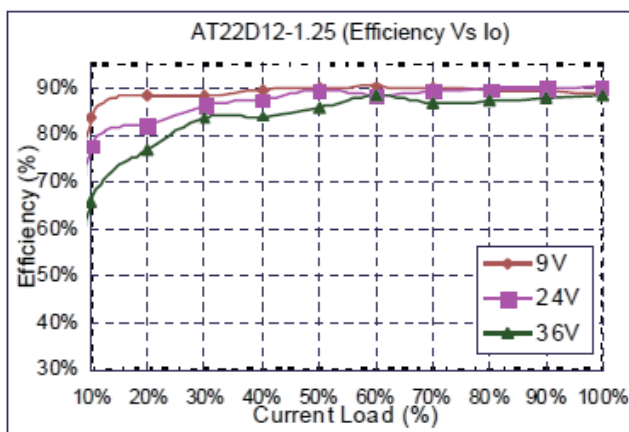
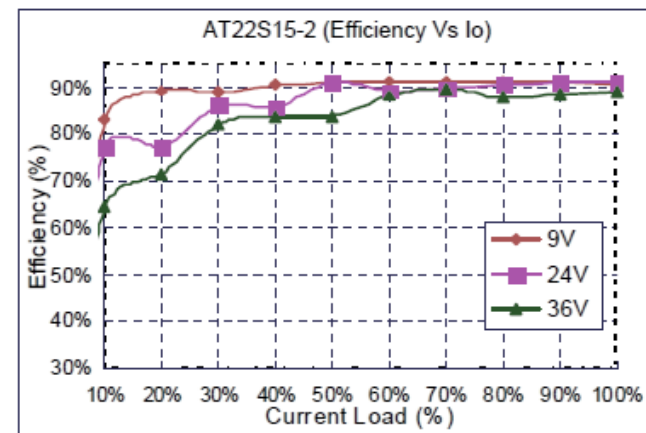
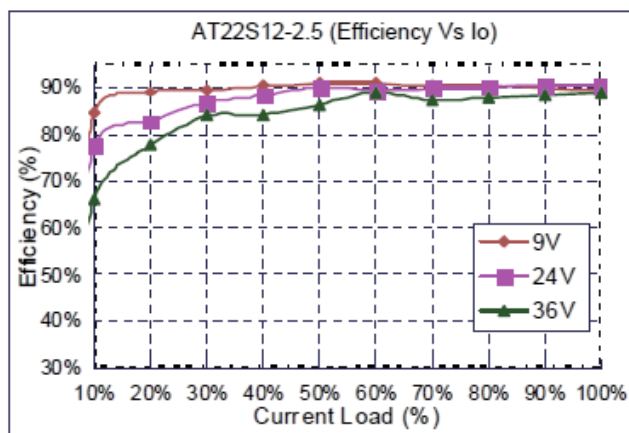
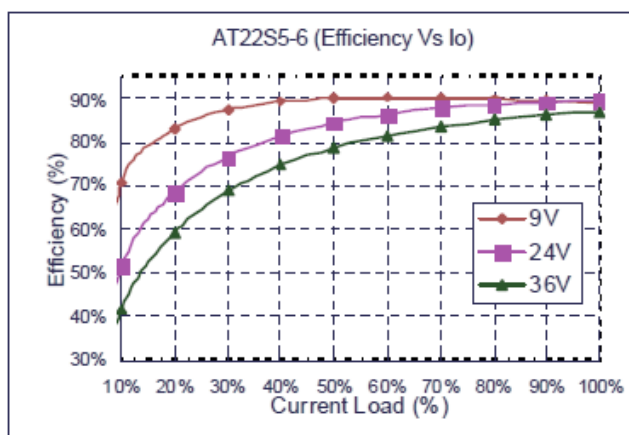
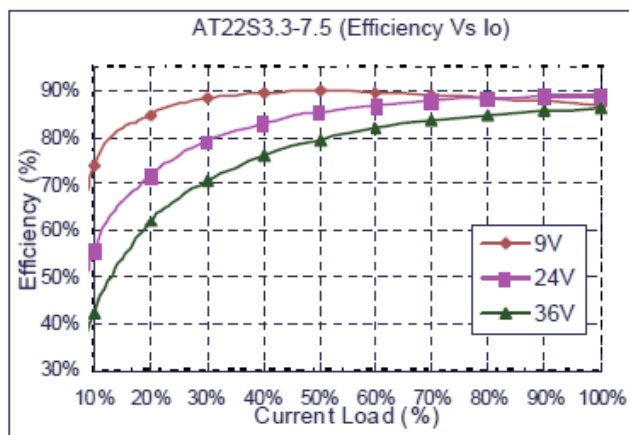
## AT Series power de-rating Curves

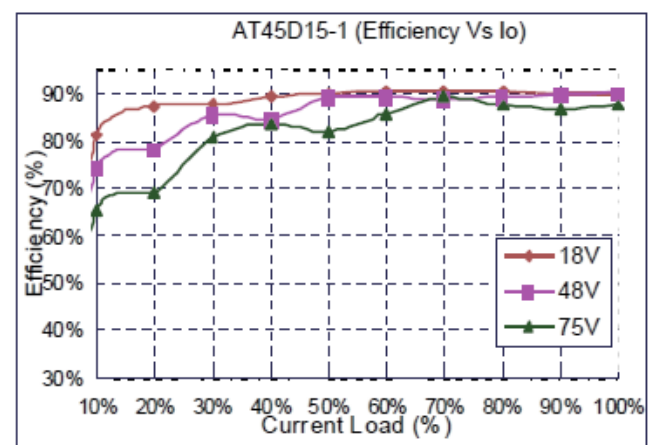
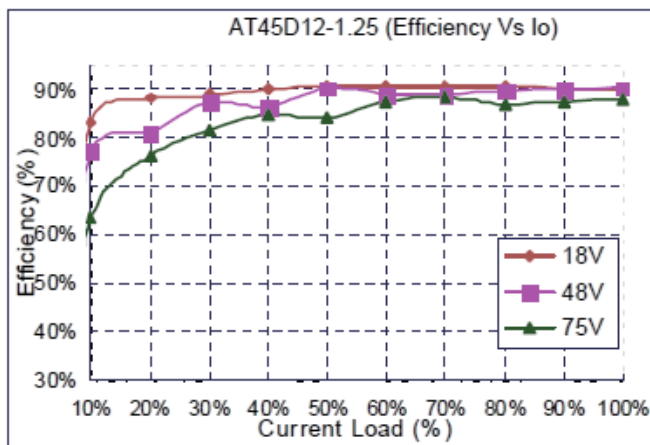
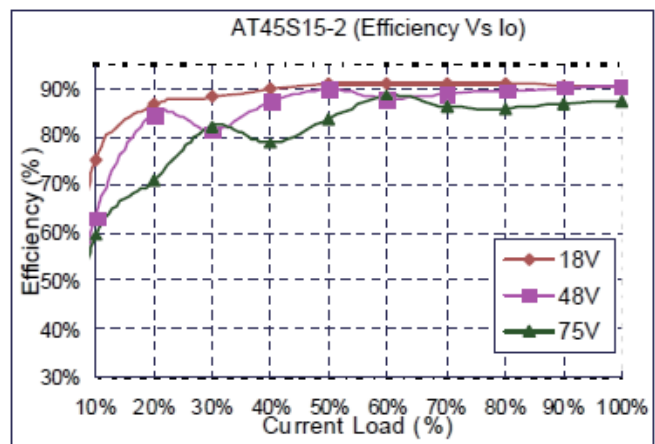
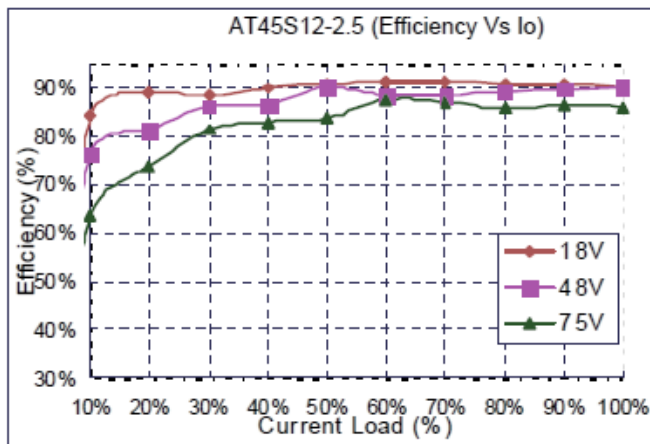
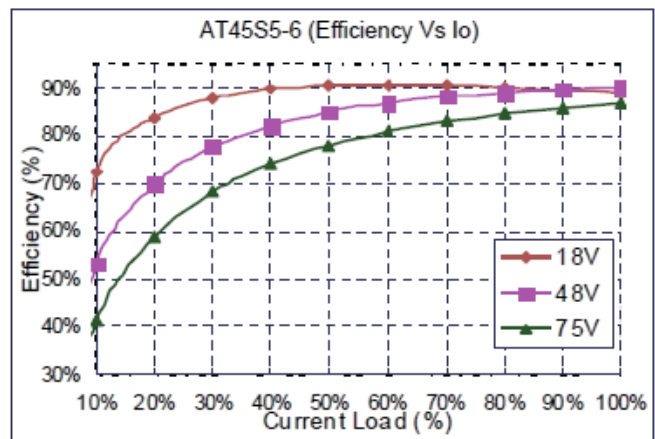
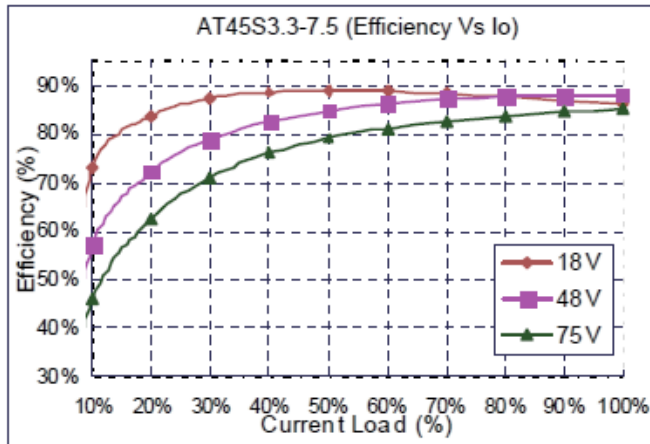
Note that the converter operating ambient temperature range is -40°C to + 85°C with derating above +65°C. Also, maximum case temperature under any operating condition should not exceed +105°C.

Typical Derating curve for Natural Convection



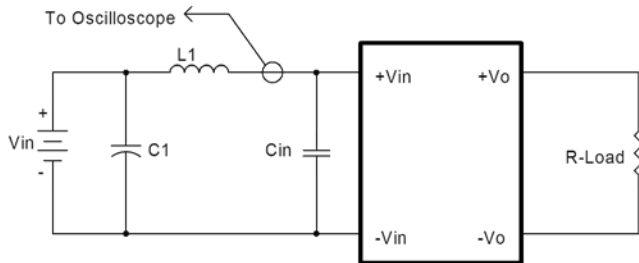
## Efficiency vs. Load Curves





## Input Capacitance at the Converter

In order to avoid problems with loop stability, the converter must be connected to a low impedance AC source and a low inductance source. The input capacitors (C<sub>in</sub>) should be placed close to the converter input pins to de-couple distribution inductance. The external input capacitors should have low ESR in order to quiet any ripple. Circuit AT shown in the figure below represents typical measurement methods for reflected ripple current. The capacitor C<sub>1</sub> and inductor L<sub>1</sub> simulate the typical DC source impedance. The input reflected-ripple current is measured by a current probe oscilloscope with a simulated source inductance (L<sub>1</sub>).



L<sub>1</sub>: 12uH  
C<sub>1</sub>: None  
C<sub>in</sub>: 33uF ESR < 0.7ohm @100KHz

### Input Reflected-Ripple Test Setup

## Test Set-Up

The basic test set-up to measure efficiency, load regulation, line regulation and other parameters is shown in the next figure. When testing the converter under any transient conditions, the user should ensure that the transient response of the source is sufficient to power the equipment under test. Below is the calculation of :

- 1- Efficiency
- 2- Load regulation
- 3- Line regulation

The value of efficiency is defined AT:

$$\eta = \frac{V_o \times I_o}{V_{IN} \times I_{IN}} \times 100\%$$

Where

V<sub>o</sub> is output voltage,  
I<sub>o</sub> is output current,  
V<sub>IN</sub> is input voltage,  
I<sub>IN</sub> is input current.

The value of load regulation is defined AT:

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where

V<sub>FL</sub> is the output voltage at full load  
V<sub>NL</sub> is the output voltage at 10% load

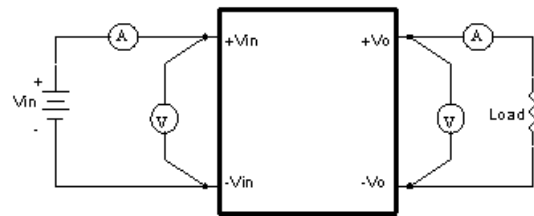
The value of line regulation is defined AT:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where

V<sub>HL</sub> is the output voltage of the maximum input voltage at full load.

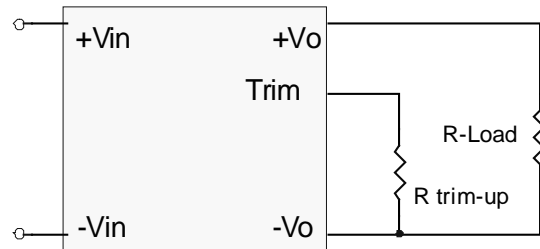
V<sub>LL</sub> is the output voltage of the minimum input voltage at full load.



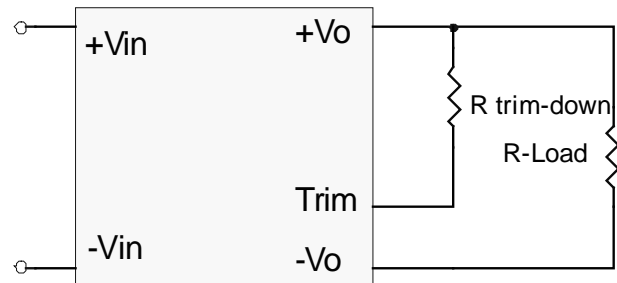
### AT Series Test Setup

## Output Voltage Adjustment (T-Option)

In order to trim the voltage up or down, the user needs to connect the trim resistor either between the trim pin and -V<sub>o</sub> for trim-up and between trim pin and +V<sub>o</sub> for trim-down. The output voltage trim range is ±10%. This is shown in the next two figures:



### Trim-up Voltage Setup



### Trim-down Voltage Setup

1. The value of R<sub>trim-up</sub> is defined as:

$$R_{trim-up} = \left( \frac{V_r \times R1 \times (R2 + R3)}{(V_o - V_{o,nom}) \times R2} \right) - R_t \text{ (K}\Omega\text{)}$$

Where

R<sub>trim-up</sub> is the external resistor in Kohm.

V<sub>o,nom</sub> is the nominal output voltage.

V<sub>o</sub> is the desired output voltage.

R1, R2, R3, R<sub>t</sub> and V<sub>r</sub> are internal to the unit and are defined in the table below

**Trim up and Trim down Resistor Values**

Model Number	Output Voltage(V)	R1 (KΩ)	R2 (KΩ)	R3 (KΩ)	Rt (KΩ)	Vr (KΩ)
AT22S3.3-7.5 AT45S3.3-7.5	3.3	2.74	1.8	0.27	9.1	1.24
AT22S5-6 AT45S5-6	5.0	2.32	2.32	0	8.2	2.5
AT22S12-2.5 AT45S12-2.5	12.0	6.8	2.4	2.32	22	2.5
AT22S15-1.25 AT45S15-1.25	15.0	8.06	2.4	3.9	2.7	25
AT22D12-1.25 AT45D12-1.25	±12V	6.8	2.4	2.32	22	2.5
AT22D15-1 AT45D15-1	±15V	8.06	2.4	3.9	2.7	25

For example, to trim-up the output voltage of the 5.0 Volts module (AT22S5-3) by 10% to 5.5V, R<sub>trim-up</sub> is calculated as follows:

$$V_o - V_{o,nom} = 5.5 - 5.0 = 0.5V$$

$$R1 = 2.32 \text{ K}\Omega$$

$$R2 = 2.32 \text{ K}\Omega$$

$$R3 = 0 \text{ K}\Omega$$

$$R_t = 8.2 \text{ K}\Omega,$$

$$V_r = 2.5 \text{ V}$$

$$R_{trim-up} = \left( \frac{2.5 \times 2.32 \times (2.32 + 0)}{0.5 \times 2.32} \right) - 8.2 = 3.4(\text{K}\Omega)$$

2. The value of R<sub>trim-down</sub> defined as:

Where

R<sub>trim-down</sub> is the external resistor in Kohm.

V<sub>o,nom</sub> is the nominal output voltage.

V<sub>o</sub> is the desired output voltage.

R1, R2, are internal to the unit and are defined in the table below.

$$R_{trim-down} = R1 \times \left( \frac{V_r \times R1}{(V_{o,nom} - V_o) \times R2} - 1 \right) - R_t \text{ (K}\Omega\text{)}$$

Where

R<sub>trim-up</sub> is the external resistor in Kohm.

V<sub>o,nom</sub> is the nominal output voltage.

V<sub>o</sub> is the desired output voltage.

R1, R2, R3, R<sub>t</sub> and V<sub>r</sub> are internal to the unit and are defined in the table above Trim down Resistor Values

For example, to trim-down the output voltage of 5.0V module (AT22S5-3) by 10% to 4.5V, R<sub>trim-down</sub> is calculated as follows:

$$V_{o,nom} - V_o = 5.0 - 4.5 = 0.5V$$

$$R1 = 2.32 \text{ K}\Omega$$

$$R2 = 2.32 \text{ K}\Omega$$

$$R3 = 0 \text{ K}\Omega$$

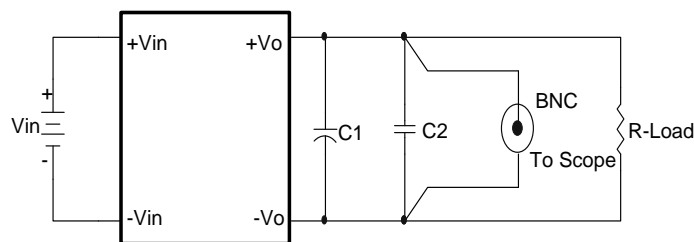
$$R_t = 8.2 \text{ K}\Omega$$

$$V_r = 2.5 \text{ V}$$

$$R_{trim-down} = 2.32 \times \left( \frac{(2.5 \times 2.32)}{0.5 \times 2.32} - 1 \right) - 8.2 = 1.08 \text{ (K}\Omega\text{)}$$

## Noise Measurement and Output Ripple

The test set-up for noise and ripple measurements is shown in the figure below. A coaxial cable was used to prevent impedance mismatch reflections disturbing the noise readings at higher frequencies. Measurements are taken with the output appropriately loaded and all ripple/noise specifications are from 0Hz to 20MHz Bandwidth.



**Output Voltage Ripple and Noise Measurement Set-Up**

Note: C1: None

C2: 0.1μF ceramic capacitor

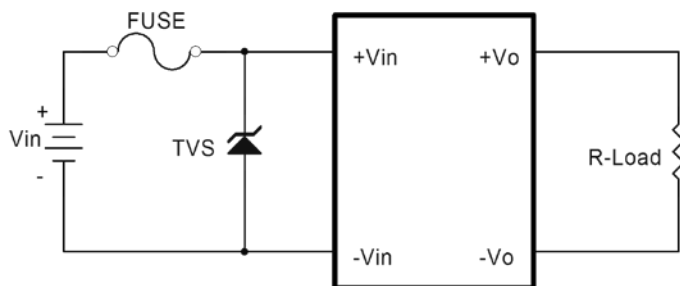
## Output Capacitance

This series of converters provides unconditional stability with or without external capacitors. For good transient response, low ESR output capacitors should be located close to the point of load.

## SAFETY and EMC

### Input Fusing and Safety Considerations

The AT series of converters do not have an internal fuse. However, to achieve maximum safety and system protection, always use an input line fuse. DATEL recommended a time delay fuse of 6A for 24Vin models and 3A for 48Vin modules. The circuit in the figure below is recommended by a Transient Voltage Suppressor diode across the input terminal to protect the unit against surge or spike voltage and input reverse voltage.

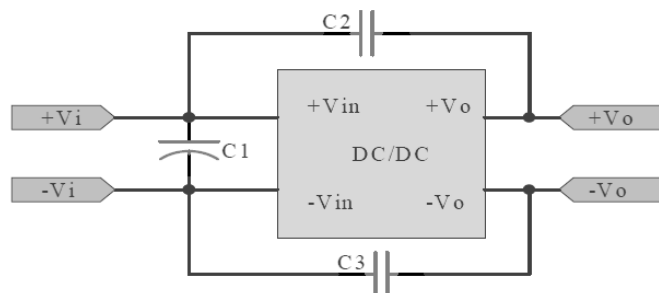


Input Protection Circuit

### EMC Considerations

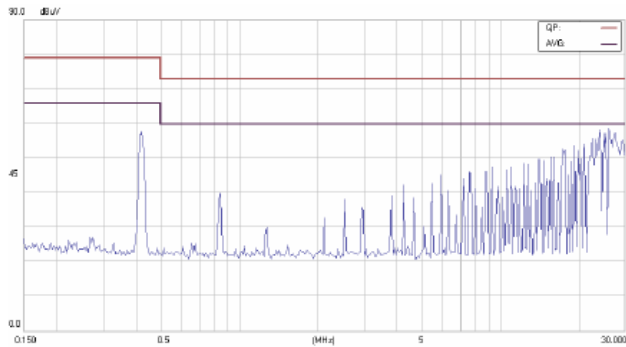
EMI Test standard: EN55022 Class A and B Conducted Emission

Test Condition: Input Voltage: Nominal, Output Load: Full Load

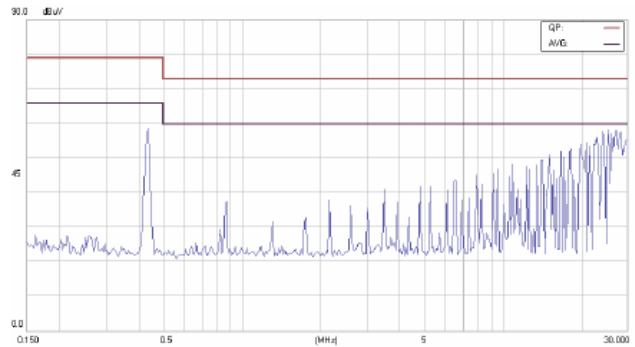


Connection circuit for conducted EMI testing

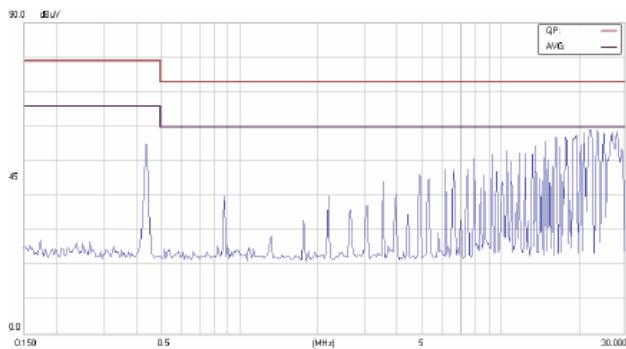
MODEL	C1	C2	C3
AT22S3.3-7.5	4.7uF/50V 1812	1000pF/3KV 1808	NC
AT22S5-6	4.7uF/50V 1812	1000pF/3KV 1808	NC
AT22S12-2.5	4.7uF/50V 1812	1000pF/3KV 1808	NC
AT22S15-2	4.7uF/50V 1812	1000pF/3KV 1808	NC
AT22D12-1.25	4.7uF/50V 1812	1000pF/3KV 1808	NC
AT22D15-1	4.7uF/50V 1812	1000pF/3KV 1808	NC
AT45S3.3-7.5	2.2uF/100V 1812	1000pF/3KV 1808	NC
AT45S5-6	2.2uF/100V 1812	1000pF/3KV 1808	NC
AT45S12-2.5	2.2uF/100V 1812	1000pF/3KV 1808	NC
AT45S15-2	2.2uF/100V 1812	1000pF/3KV 1808	NC
AT45D12-1.25	2.2uF/100V 1812	1000pF/3KV 1808	NC
AT45D15-1	2.2uF/100V 1812	1000pF/3KV 1808	NC



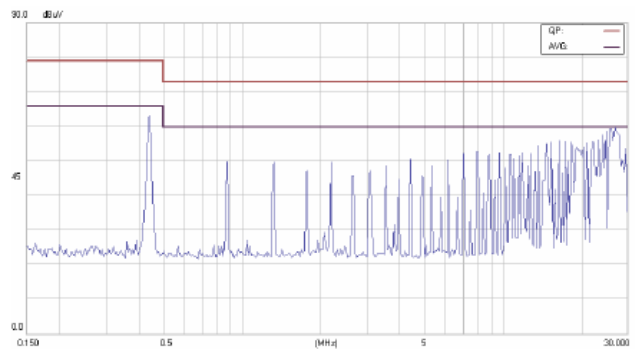
Class A Test Conducted for AT22S3.3-7.5



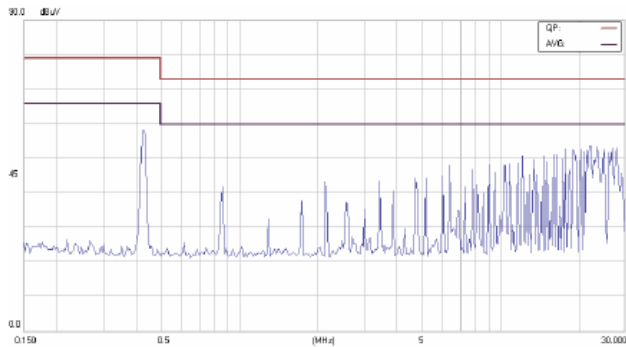
Class A Test Conducted for AT22S5-6



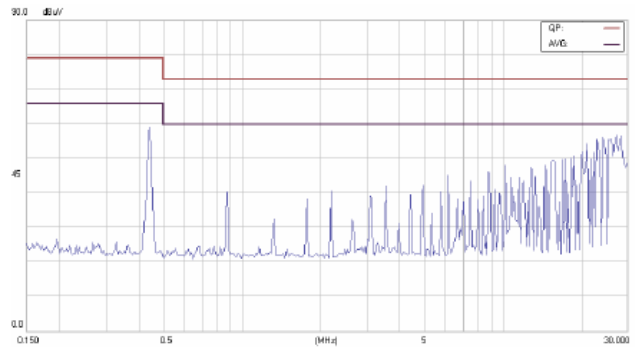
Class A Test Conducted for AT22S12-2.5



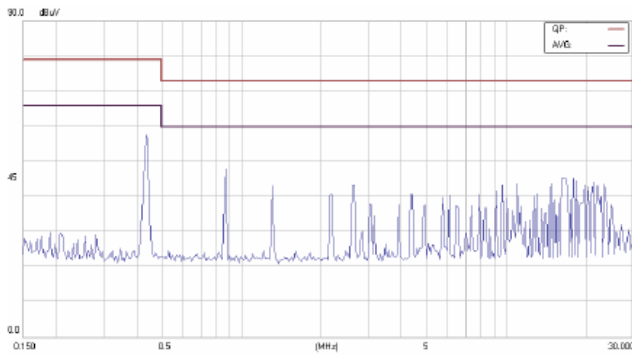
Class A Test Conducted for AT22S15-2



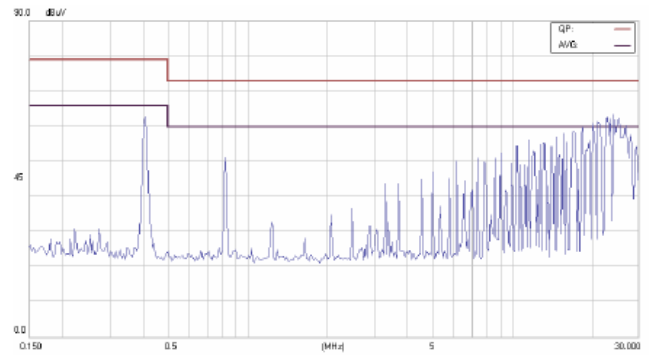
Class A Test Conducted for AT22D12-1.25



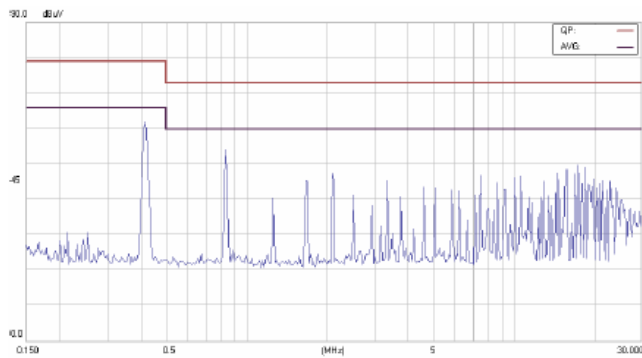
Class A Test Conducted for AT22D15-1



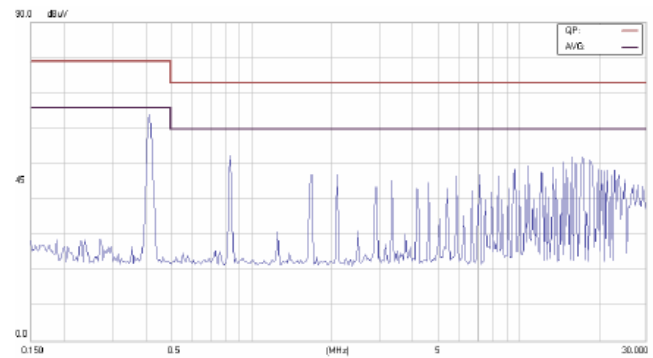
Class A Test Conducted for AT45S3.3-7.5



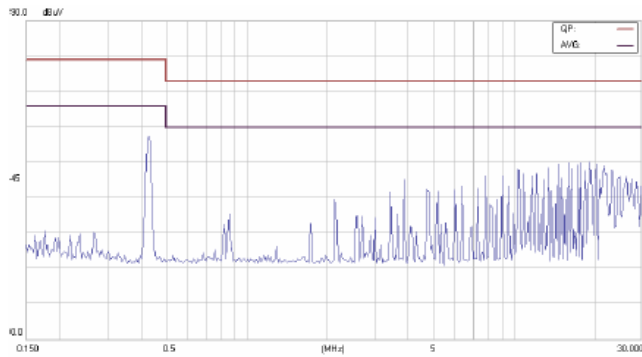
Class A Test Conducted for AT45S5-6



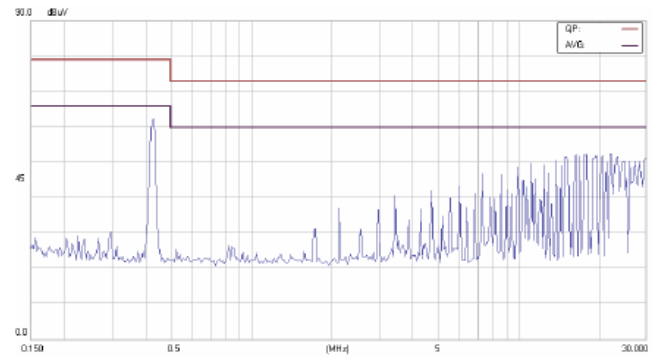
Class A Test Conducted for AT45S12-2.5



Class A Test Conducted for AT45S15-2



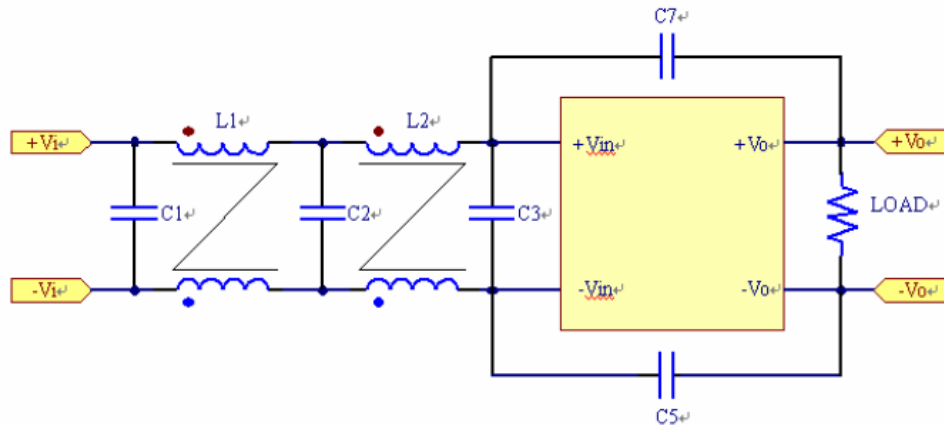
Class A Test Conducted for AT45D12-1.25



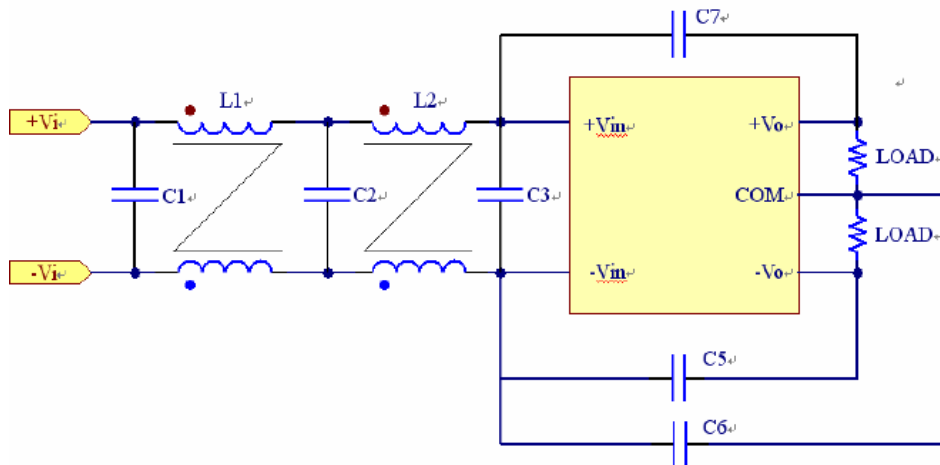
Class A Test Conducted for AT45D15-1

## EMC Considerations

EMI Test standard: EN55022 Class B Conducted Emission  
Test Condition: Input Voltage: Nominal, Output Load: Full Load



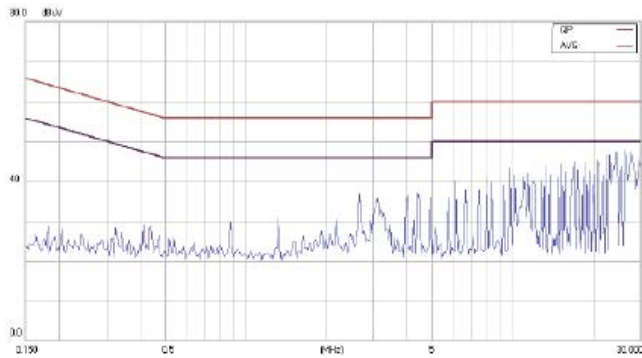
Single Output



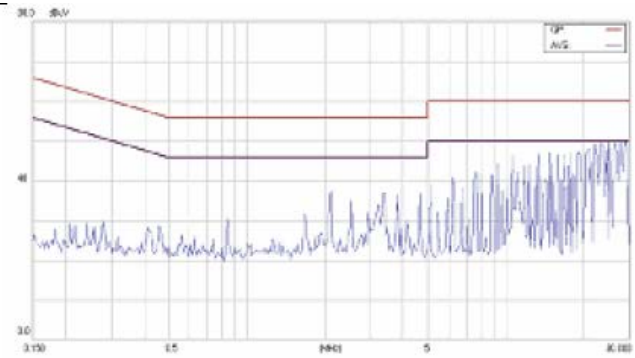
Dual Output

Connection circuit for conducted EMI testing

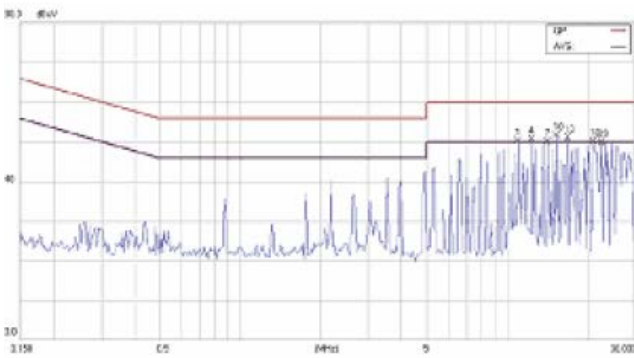
MODEL	C1	C2	C3	C5	C6	C7	L1	L2
AT22S3.3-7.5	4.7uF/50V 1812	4.7uF/50V 1812	4.7uF/50V 1812	1000pF/2000V 1206	1000pF/2000V 1206	1000pF/2000V 1206	400uH Common Choke	150uH Common Choke
AT22S5-6	4.7uF/50V 1812	4.7uF/50V 1812	4.7uF/50V 1812	1000pF/2000 V 1206	1000pF/2000V 1206	1000pF/2000V 1206	400uH Common Choke	150uH Common Choke
AT22S12-2.5	4.7uF/50V 1812	4.7uF/50V 1812	4.7uF/50V 1812	1000pF/2000 V 1206	1000pF/2000V 1206	1000pF/2000V 1206	400uH Common Choke	150uH Common Choke
AT22S15-2	4.7uF/50V 1812	4.7uF/50V 1812	4.7uF/50V 1812	1000pF/2000 V 1206	1000pF/2000V 1206	1000pF/2000V 1206	400uH Common Choke	150uH Common Choke
AT22D12-1.25	4.7uF/50V 1812	4.7uF/50V 1812	4.7uF/50V 1812	1000pF/2000 V 1206	1000pF/2000V 1206	1000pF/2000V 1206	400uH Common Choke	150uH Common Choke
AT22D15-1	4.7uF/50V 1812	4.7uF/50V 1812	4.7uF/50V 1812	1000pF/2000 V 1206	1000pF/2000V 1206	1000pF/2000V 1206	400uH Common Choke	400uH Common Choke



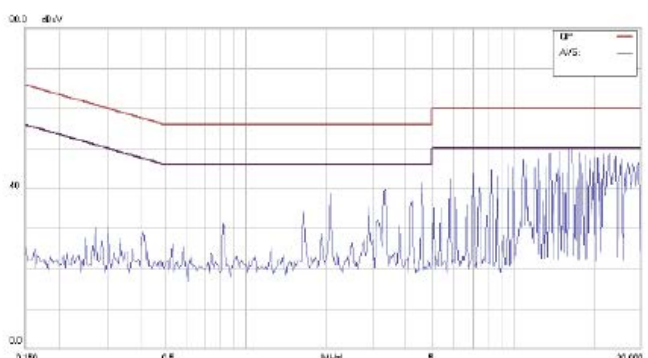
Class B Conducted Test for AT22S3.3-7.5



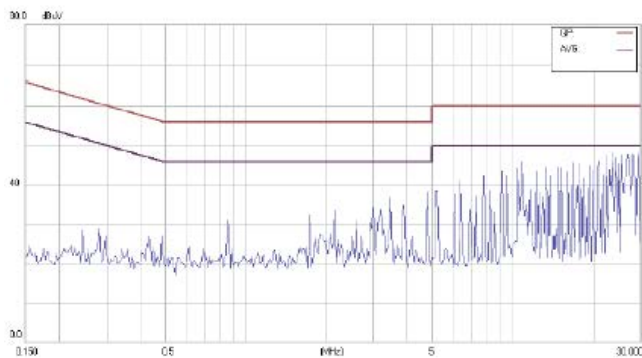
Class B Conducted Test for AT22S5-6



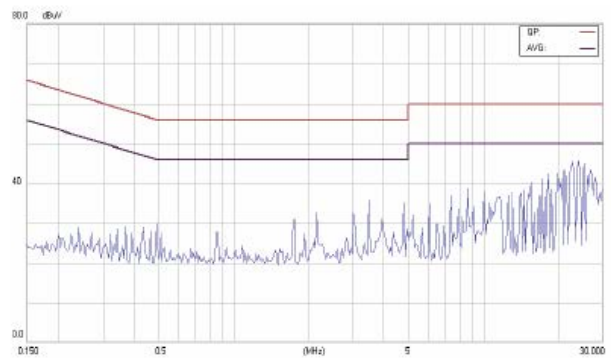
Class B Conducted Test for AT22S12-2.5



Class B Conducted Test for AT22S15-2

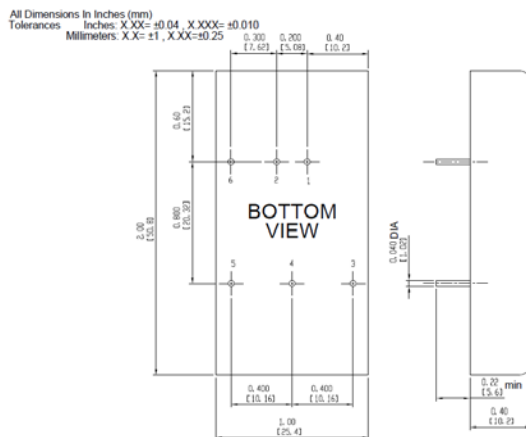


Class B Conducted Test for AT22D12-1.25



Class B Conducted Test for AT22D15-1

## MECHANICAL DIMENSIONS Inches (mm)

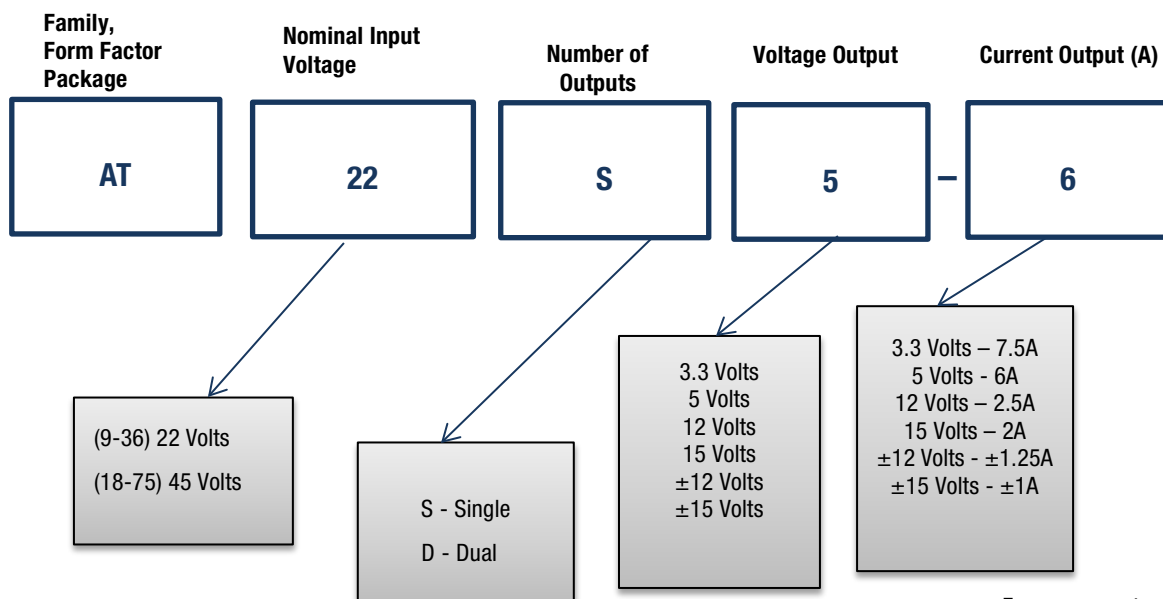


Note: All dimensions are in inches (millimeters). Tolerance: x.xx ±0.04 in. (0.5mm), x.xxx ±0.010 in. (0.25 mm) unless otherwise noted

## PIN CONNECTIONS

Pin Connections		
PIN	SINGLE OUTPUT	DUAL OUTPUT
1	+ V Input	+ V Input
2	- V Input	- V Input
3	+ V Output	+ V Output
4	Trim	- V Output
5	- V Output	Common
6	Remote On/Off	Remote On/Off

## PART NUMBER AND ORDERING INFORMATION



For proper part ordering, enter option  
suffixes in order listed in table above